

AEROSPACE DATA EXCHANGE PROGRAM TRANSMITTAL  
**PRODUCT CHANGE NOTICE**



<b>1. TITLE</b> MICROCIRCUIT, MEMORY, DIGITAL, CMOS, 1MEG x 32-BIT (32M), RADIATION-HARDENED, DUAL VOLTAGE SRAM with embedded EDAC, MULTICHIP MODULE		<b>2. DOCUMENT NUMBER</b> SPO-2012-PCN-0008	
<b>4. MANUFACTURER NAME AND ADDRESS</b> CAES 4350 CENTENNIAL BOULEVARD COLORADO SPRINGS, COLORADO 80907-3486		<b>3. DATE (Year, Month, Date)</b> 2012, OCTOBER, 04	
		<b>5. MANUFACTURER POINT OF CONTACT NAME</b> Mike Leslie	
		<b>6. MANUFACTURER POINT OF CONTACT TELEPHONE</b> (719) 594-8148	
<b>8. CAGE CODE</b> 67268		<b>7. MANUFACTURER POINT OF CONTACT EMAIL</b> Mike.Leslie@cobhamaes.com	
		<b>9. EFFECTIVE DATE</b> June 15, 2010	<b>10. PRODUCT IDENTIFICATION CODE</b> QS16, QS17
<b>12. BLANK</b>		<b>13. SMD NUMBER</b> 5962-10202	<b>14. DEVICE TYPE DESIGNATOR</b> 01 - 04
		<b>15. RHA LEVELS</b> R	<b>16. QML LEVEL</b> Q, Q+, V
		<b>17. NON QML LEVEL</b> HiRel, Proto	<b>18. BLANK</b>

**19. PRODUCT CHANGE**

CAES is working in coordination with DLA Land and Maritime to revise SMD 5962-10202, revision level B, for the following changes:

**Section 1.5 (sheet 3)**

	PREVIOUS	CORRECTED
with no latch-up .....	$> 111 \text{ MeV-cm}^2/\text{mg } 7/9/$	$\leq 110 \text{ MeV-cm}^2/\text{mg } 7/9/$
Single event upset .....	$6.0 \times 10^{-16} \text{ errors/bit-day } 8/9/$	$8.1 \times 10^{-16} \text{ errors/bit-day } 8/9/$

**Table IB and note /5 (sheet 9)**

**PREVIOUS:**

Table IB. SEP test limits 1/ 2/

All	<u>5/</u>	<u>5/</u>	>111
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5/ Soft error rate =  $6.0 \times 10^{-16}$  error/bit-day assuming .....

**CORRECTED:**

Table IB. SEP test limits 1/ 2/

All	<u>5/</u>	<u>5/</u>	≤110
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5/ Soft error rate =  $8.1 \times 10^{-16}$  error/bit-day assuming .....

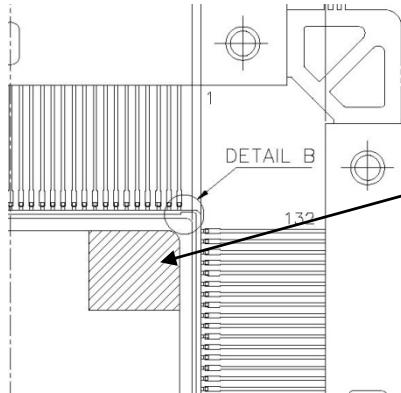
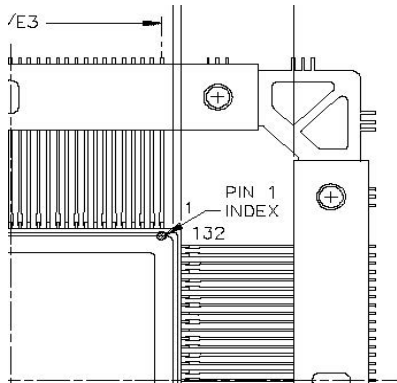
<b>20. DISPOSITIONARY RECOMMENDATION:</b>		USE AS IS <input type="checkbox"/>	CONTACT MANUFACTURER <input type="checkbox"/>	REMOVE & REPLACE <input type="checkbox"/>	CHECK & <input checked="" type="checkbox"/> USE AS IS
<b>21. ADEPT REPRESENTATIVE</b> Timothy L. Meade		<b>22. SIGNATURE</b> 			<b>23. DATE</b> 2012, October, 04

**ADDED: Alternative marking as package pin one identifier to FIGURE 1. Case outline (sheet 13).**

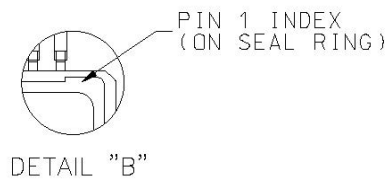
**CORRECTED: Drawing of original pin one identifier corrected and Detail B added.**

**PREVIOUS:**

**CORRECTED:**



(SEE NOTE 9)



**ADDED: Note 9 (sheet 14):**

9/. A dot may be marked on lid to indicate pin 1 index within area shown.